



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.10/044,206
Filing Date January 11, 2002
Inventor.....Jerome Michael Eldridge
AssigneeMicron Technology, Inc.
Group Art Unit 2813
Examiner Laura M. Schillinger
Attorney's Docket No. MI22-1914
TitleMethods of Forming Void Regions, Dielectric Regions and Capacitor
Constructions

RESPONSE TO ADVISORY ACTION DATED JANUARY 12, 2004

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Responsive to the Advisory Action dated January 12, 2004, Applicant
amends and remarks as follows:

AMENDMENTS



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	First Named Inventor	Jerome Michael Eldridge
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Firm or Individual name	Satheesh K. Karra, Reg. No. 40,246 Wells St. John P.S.
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